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1	101	((708/603.ccls. and @ad<19990120) and adder and multiplier)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/20 08:03
2	1	("5740249").PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/20 08:03
-	1	"long product-sum"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/04 11:13
-	25	elgamal.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 15:44
-	2	((("5740249") or ("6662201")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 08:28
-	2525	double adj precision	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 08:28
-	15	(double adj precision near add\$3) and (single adj precision near multipl\$7)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 14:59
-	129	708/603.ccls. and @ad<19990120	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 13:29
-	1	("6293874").PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/13 13:29
-	1	long adj product adj sum	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:11
-	245	processor and crypto\$6 and rsa and "elliptic curve"	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:11
-	14	processor and crypto\$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:26
-	4	@ad<19990120 and processor and crypto\$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 09:46
-	3	@ad<19990120 and processor and crypto\$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer and (carry\$3 carries) and propagat\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 15:20
-	1	5664099.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/18 14:00
-	2	@ad<19990120 and processor and crypto\$6 and rsa and "elliptic curve" and adder and multiplier and ((finite galois) adj field) and integer and (carry\$3 carries) and propagat\$3 and (memory buffer\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/05/19 07:03

-	1	jp-09311854-a.did.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 08:40
-	1	("4745568").PN.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 08:41
-	1	("6141420").PN.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 08:42
-	1	((("6141420").PN.) and modul??	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 08:42
-	<i>abstracts</i> 75	@ad<19990120 and ("finite field" galois "GF(2.sup.m)" "GF(2^m)") and (modular adj (multiplication exponentiation))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/05/19 08:43
-	<i>kwic abstract</i> 2	@ad<19990120 and ("finite field" galois "GF(2.sup.m)" "GF(2^m)") and (modular adj (multiplication exponentiation))) and (product adj sum)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/05/19 09:58
-	1	@ad<19990120 and 708/603.ccls. and "modular multiplication"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/05/19 09:59
-	<i>abstracts + kwic</i> 15	((double adj precision near add\$3) and (single adj precision near multipl\$7) and ((single double) adj precision) and (multiplier adder)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:02
-	0	((double adj precision near add\$3) and (single adj precision near multipl\$7) and ((single double) adj precision) and (multiplier adder)) and 708/603.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:02
-	163	708/603.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:02
-	53	708/603.ccls. and precision	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:03
-	<i>title scanned</i> 39	(708/603.ccls. and precision) and @ad<19990120	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:03
-	129	708/603.ccls. and @ad<19990120	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:04
-	101	(708/603.ccls. and @ad<19990120) and adder and multiplier	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/19 15:25
-	<i>abstracts + kwic</i> 10	((708/603.ccls. and @ad<19990120) and adder and multiplier) and ((single double) adj precision)	USPAT; US-PGPUB; EPO; JPO; IBM TDB	2004/05/20 07:32

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... This definitely is not the most efficient way to implement a **full adder**, but it is ... so inclined, see what you can do to implement this logic with fewer **gates**. ...
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... you can use a NAND **gate** in place of other **gates**, so you end up with a smaller circuit...how would I go about making a **Full Adder** circuit using only NAND **gates**? ...
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Logic Gates and Circuits

... the wire for the input a leading to the the AND **gate**). The above circuit has a special name: it is a "half **adder**". It differs from a "**full adder**" in that it is ...
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FULL ADDER

... One method of constructing a **full adder** is to use two half **adders** and an OR **gate** as shown in figure 3-8. The inputs A and B are applied to **gates** 1 and 2. These ...
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Half and Full Adders

... Half and **Full Adders**. From basic **gates**, we will develop a **full adder** circuit that adds two binary numbers. Consider adding two 2-bit binary numbers and
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FullAdder CMOS

... Binary **adder**. The "**Full Adder**" cell (FA) is made of two connected complex **gates**. It realises an arithmetic equality: the weighted ...
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Simple Logic Gates

... Optional Exercise: Draw a complete logic circuit for the **full adder** using 'AND', 'NOT', and 'OR' **gates** as appropriate. Adding Binary Numbers. ...
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Philips Semiconductors - Catalog - Full adders

... Product info on 74F283 74F283, 4-bit binary **full adder** with fast carry. **Gates** - [155] - Static table for **Gates** - Application notes available in branch **Gates**. ...
www.semiconductors.philips.com/catalog/219/282/27063/27227/ - 101k - [Cached](#) - [Similar pages](#)

Full-adders

... Innocent as they look here, **full-adders** are in fact built up ... themselves can be built up from five NAND **gates** ("a" above needs only be a half-**adder**, as no ...
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Boolean Algebra

... an AND **gate** with a NOT **gate** (in that order) to form a NAND **gate**. ... The implementation of a Half **Adder** from basic logic elements is very easy to work ... **Full Adder**. ...

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09487483
Michael J. Simitoski
Michael.Simitoski@uspto.gov
(703) 305-8191

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